	Type 1	Type 2	Type 3
Number of chips	2	3	3
X_MAX	127	191	191
Y_MAX	63	63	63
Pin 1	V_{SS}	DB7	V_{SS}
Pin 2	V_{DD}	DB6	V_{DD}
Pin 3	V_o	DB5	V_o
Pin 4	RS	DB4	RS
Pin 5	R/W	DB3	R/W
Pin 6	E	DB2	E
Pin 7	DB0	DB1	DB0
Pin 8	DB1	DB0	DB1
Pin 9	DB2	E	DB2
Pin 10	DB3	R/W	DB3
Pin 11	DB4	RS	DB4
Pin 12	DB5	V_{out}	DB5
Pin 13	DB6	V_{DD}	DB6
Pin 14	DB7	V_{SS}	DB7
Pin 15	CS1	CS2	CS1
Pin 16	CS2	CS1	/RST
Pin 17	/RST	V_{out}	CS2
Pin 18	V_{out}	/RST	CS3
Pin 19	LED A	LED A	V_{out}
Pin 20	LED K	LED K	LED A

Explanation	
V_{SS}	GND
V_{DD}	+5V power supply
V_o	Operating voltage for LCD (input)
V_{out}	Negative voltage for LCD (output)
RS	Register selection (high: data, low: instruction)
R/W	Read/Write selection (high: read, low: write)
E	Enable signale (high -> low)
/RST	Reset (low = active)
CS1	Panel selection
CS2	Type 1 and 3: low signal on CS1, CS2 or CS3 selects corresponding panel
CS3	Type 2: Panel 1 (CS1 low, CS2 low), Panel 2 (CS1 high, CS2 low), Panel 3 (CS1 low, CS2 high)